

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION  
USING AN APPLICATION DATA SHEET (37 CFR 1.76)**

I/we, the below named inventor(s), I/we declare that:

This declaration is directed to:

- ☐ The attached application, or
- ☒ Application No. 09/797,863, filed on March 1, 2001,
- ☐ as amended on \_\_\_\_\_ (if applicable);

I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;

I/ we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including material information which became available between the filing date of the prior application and the National or PCT International filing date of the continuation-in-part application, if applicable; and

All statements made herein of my/own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

**FULL NAME OF INVENTOR(S)**

Inventor one Peter Rabkin Date: 05/15/2001

Signature: Peter Rabkin Citizen of: US

Inventor two Hsingya Arthur Wang Date: 05/18/2001

Signature: Hsingya Arthur Wang Citizen of: US

Inventor three Kai-Cheng Chou Date: 05/15/2001

Signature: Kai-Cheng Chou Citizen of: Taiwan

Inventor four \_\_\_\_\_ Date: \_\_\_\_\_

Signature: \_\_\_\_\_ Citizen of: \_\_\_\_\_

☐ Additional inventors are being named on \_\_\_\_\_ additional form(s) attached hereto.

**Burden Hour Statement:** This collection of information is required by 35 U.S.C. 115 and 37 CFR 1.63. The information is used by the public to file (and the PTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 1 minute to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

PA 3144275 v1



Please type a plus sign inside this box → ☐

PTO/SB/81 (02-01)

Approved for use through 10/31/2002. OMB 0651-0035

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

<b>POWER OF ATTORNEY OR AUTHORIZATION OF AGENT</b>	<b>Application Number</b>	09/797,863
	<b>Filing Date</b>	March 1, 2001
	<b>First Named Inventor</b>	Peter Rabkin et al.
	<b>Title</b>	Transistor and Memory Cell with Ultra-Short Gate Feature and Method of Fabricating the Same
	<b>Group Art Unit</b>	2812
	<b>Examiner Name</b>	
	<b>Attorney Docket Number</b>	00939A-079300US

I hereby appoint:

☒ Practitioners at Customer Number **20350** → 

Place Customer  
Number Bar Code  
Label here

☐ Practitioner(s) named below:

Name	Registration Number

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

Please change the correspondence address for the above-identified application to:

☐ The above-mentioned Customer Number.

**OR**

<input type="checkbox"/> Firm or Individual Name			
Address			
Address			
City	State	ZIP	
Country			
Telephone	Fax		

I am the:

☐ Applicant/Inventor.

☒ Assignee of record of the entire interest. See 37 CFR 3.71.  
*Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).*

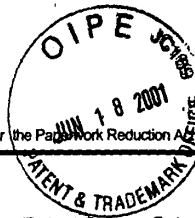
**SIGNATURE of Applicant or Assignee of Record**

Name	Hyun Goo Choi, Senior Manager, Hynix Semiconductor, Inc.
Signature	<i>Hyun Goo Choi</i>
Date	<i>June 2, 2001</i>

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below\*.

☐ \*Total of \_\_\_\_\_ forms are submitted.

Burden Hour Statement: This form is estimated to take 3 minutes to complete. Time will vary depending upon the needs of the individual case. Any Comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231. PA 3144281 v1



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PTO/SB/96 (08-00)  
Approved for use through 10/31/2002. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

#3

### STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Peter Rabkin et al.

Application No./Patent No.: 09/797,863

Filed/Issue Date: March 1, 2001

Entitled: Transistor and Memory Cell with Ultra-Short Gate Feature and Method of Fabricating the Same

Hynix Semiconductor, Inc., a corporation of the Republic of Korea

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of an undivided part interest

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: \_\_\_\_\_ To : \_\_\_\_\_  
The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.
2. From: \_\_\_\_\_ To : \_\_\_\_\_  
The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.
3. From: \_\_\_\_\_ To : \_\_\_\_\_  
The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

June 2, 2001  
Date

Hyun Goo Choi  
Signature

Hyun Goo Choi  
Typed or printed name

Senior Manager  
Title

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

PA 3144284 v1

## ASSIGNMENT OF PATENT APPLICATION

JOINT

WHEREAS, Peter Rabkin, of 21631 Regnart Road, Cupertino, California 95014; Hsingya Arthur Wang, of 295 Cresta Vista Way, San Jose, California 95119; and Kai-Cheng Chou, of 3661 Jasmine Circle, San Jose, California 95135; hereinafter referred to as "Assignors," are the inventors of the invention described and set forth in the below-identified application for United States Letters Patent:

Title of Invention: **Transistor and Memory Cell with Ultra-Short Gate Feature and Method of Fabricating the Same**

Filing Date: **March 1, 2001**

Application No.: **09/797,863; and**

WHEREAS, Hynix Semiconductor, Inc., a corporation of the Republic of Korea, located at San 136-1, Ami-ri, Bubal-eub, Ich'on-si, Kyoungki-do, 467-701, Korea, hereinafter referred to as "Assignee," is desirous of acquiring an interest in the invention and application and in any U.S. Letters Patent and Registrations which may be granted on the same;

For good and valuable consideration, receipt of which is hereby acknowledged by Assignors, Assignors have assigned, and by these presents do assign to Assignee all right, title and interest in and to the invention and application and to all foreign counterparts (including patent, utility model and industrial designs), and in and to any Letters Patent and Registrations which may hereafter be granted on the same in the United States and all countries throughout the world, and to claim the priority from the application as provided by the Paris Convention. The right, title and interest is to be held and enjoyed by Assignee and Assignee's successors and assigns as fully and exclusively as it would have been held and enjoyed by Assignors had this Assignment not been made, for the full term of any Letters Patent and Registrations which may be granted thereon, or of any division, renewal, continuation in whole or in part, substitution, conversion, reissue, prolongation or extension thereof.

Assignors further agree that they will, without charge to Assignee, but at Assignee's expense, (a) cooperate with Assignee in the prosecution of U.S. Patent applications and foreign counterparts on the invention and any improvements, (b) execute, verify, acknowledge and deliver all such further papers, including patent applications and instruments of transfer, and (c) perform such other acts as Assignee lawfully may request to obtain or maintain Letters Patent and Registrations for the invention and improvements in any and all countries, and to vest title thereto in Assignee, or Assignee's successors and assigns.

IN TESTIMONY WHEREOF, Assignors have signed their names on the dates indicated.

Dated: 05/15/2001

Peter Rabkin

Peter Rabkin

Dated: 05/18/2001

Hsingya Arthur Wang

Hsingya Arthur Wang

Dated: 05/15/2001

Kai-Cheng Chou

Kai-Cheng Chou

Witnesses:

John Chang

Timothy M. Gallagher

Timothy Gallagher

PA 3144278 v1

**COPY**